
BIOGRAPHICAL SKETCH

Provide the following information for the key personnel and other significant contributors.
Follow this format for each person. **DO NOT EXCEED FOUR PAGES.**

NAME Steve Claffey	POSITION TITLE Chief Hardware Engineer WinProbe Corporation		
eRA COMMONS USER NAME			
EDUCATION/TRAINING <i>(Begin with baccalaureate or other initial professional education, such as nursing, and include postdoctoral training.)</i>			
INSTITUTION AND LOCATION	DEGREE <i>(if applicable)</i>	YEAR(s)	FIELD OF STUDY
University of Miami, Coral Gables, FL	BS	1982	Electrical Engineering Magna Cum Laude

A. Positions and Honors.

- 2006– Present Chief Hardware Engineer of WinProbe Corporation. Responsible for product concept and programming Field Programmable Gate Arrays, Schematic Design and developing advanced ultrasonic scanner for commercialization. The company is based on placing as much of the intellectual property and functionality in FPGAs.
- 2005 – 2006 Field Applications Engineer for NU HORIZONS ELECTRONICS, Ft. Lauderdale, FL, a Global specialty semiconductor distributor. Directed all technical aspects of sales process for 35 different semiconductor companies, including customer and internal training, design assistance, architectural recommendations, customer support, and problem resolution. Created training material and taught statewide seminars on the latest FPGA design techniques including those used for embedded processor and digital signal processing applications. Provided product training, design guidance and debug assistance for technical organizations and electronic OEMs located between Cape Canaveral and Miami.
- 2003 –2005 Consultant Provided consulting design services for several south Florida companies including Precision Analog Systems, Crossmatch Technologies, and WinProbe Corporation.
- 1994 – 2003 Field Applications Engineer. Insight Electronics , Boca Raton, FL. Directed all technical aspects of sales process for 25 different semiconductor companies, including customer and internal training, design assistance, architectural recommendations, customer support, and problem resolution. Collaborated with sales management to formulate strategies for the sales force that would maximize local profits.
- 1992 – 1994 Design Engineer. Advanced Promotion Technologies, Pompano Beach Fl, Designed hardware for start-up company. Architected third generation hardware platform and communication subsystems, reducing system costs and increasing performance. Designed FPGA-based LCD controller that provided special video effects such as smooth scrolling (horizontal and vertical) and multiple picture-in-picture windows. Engineered both digital and analog circuits while also writing assembly code and C-based applications.
- 1985 –1992 Senior Engineer Checkrobot, Deerfield Beach, FL. (Producer of automated self-checkout equipment for retail applications) Designed hardware and provided trade show support for start-up company. Designed patented security system that validated each item after it was scanned, and a proprietary local area network, providing central components of system. Provided complete tactical support for

international and domestic trade shows, from system setup to live demonstrations for public and press. Created both digital and analog circuits while also writing assembly code and C-based applications.

1982 –1985 Project Engineer Burroughs Corporation, Corral Springs, FL. (Worldwide information technology services and solutions). Designed Character Recognition electronics for use in automated banking equipment. Designed hardware and firmware. Designed “Character Recognition” hardware for use in high speed check sorting equipment used by the Federal Reserve.

C. Research Support

Ongoing Research Support

LOW COST MULTI-MODAL ARRAY BASED SMALL ANIMAL SCANNER R44EB012429 07/01/2010 - 06/30/2012 NIH NIBIB

Chief Hardware Engineer

“High Frequency Imaging of Mice.”

Completed Research Support

Integrated Multifunctional Imaging of Deep Vein Thrombosis 1 R44 HL 091609-01 01/01/08 – 6/30/2010 NIH/HLB

Chief hardware Engineer

“Estimate the age of the clot by elastography and photoacoustics” Role: Key personnel

Authorizer Project

Agency/Type: NIST ATP

Low cost ultrasonic fingerprint and biological detector for commerce and security

Optoacoustic and Ultrasonic Imaging of Angiogenesis 1 R44 CA110079-01 08/19/04-12/31/08 NIH/NIC
Imaging of Hb and HbO in neo-vascular structures by photonic adsorption and emission of ultrasound.

Chief Hardware Engineer.